

Application No.: 09/922,046

Docket N .: JCLA6385

**AMENDMENTS****In The Specification:**

Please amended paragraph beginning at page 6, line 3 as follows:

(currently amended) In one embodiment of the invention, the bridge I 230 is further coupled to the accelerated graphics port bus II 225 and the PCI bus III 255. By the bridge I 230, the signal and data of the accelerated graphics port bus I 220 and the accelerated graphics port bus II ~~[[255]]~~ 225 are mutually and compatibly converted and buffered to retain the accelerated graphics port bus for the computer system. Similarly, the bridge I 230 further compatibly converts the data and signal of the accelerated graphics port bus I 220 and the PCI bus III 255, so that an additional extended bus is obtained.